

ABSTRACT

DEVICE TRANSFER MECHANISM FOR A TEST HANDLER

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The invention provides a mechanism for a test handler using for electrical testing of electronic devices. The devices are placed on a platform configured to move semiconductor devices from an onloading position to an offloading position along a predetermined path. A transfer arm with a plurality of transfer
10 heads connected to it is located adjacent the path. The transfer heads are configured to pick up and transfer semiconductor devices from the platform to a testing position for testing, and thereafter to transfer the semiconductor devices from the testing position to the platform for offloading.

15 (Figure 1)